

11017 U.S. PTO
09/998326
12/03/01

Class	Subclass	ISSUE CLASSIFICATION

BEST AVAILABLE COPY

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. SCANNED 746 ① Q.A. MIT	PATENT DATE
------------------------------------	-------------

APPLICATION NO. 09/998326	CONT/PRIOR F	CLASS 257 714	SUBCLASS 724	ART UNIT 2811 2133	EXAMINER TON
------------------------------	-----------------	---------------------	-----------------	--------------------------	-----------------

APPLICANTS
TITLE

Shunsuke Endou

Semiconductor device capable of easily setting test mode during test conducted by applying high voltage

PTO-2040
1299

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)						
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION								

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER <input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	DRAWINGS Sheets Drwg. Figs. Drwg. Print Fig.			CLAIMS ALLOWED Total Claims Print Claim for O.G.	
	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED _____	
	_____ (Primary Examiner) (Date)			ISSUE FEE Amount Due Date Paid	
	_____ (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER _____	

WARNING:

The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

Form PTO-436A
(Rev. 6/99)

FILED WITH: ☐ DISK (CRF) ☐ FICHE ☐ CD-ROM
(Attached in pocket on right inside flap)